Appl. No. 09/702, 462
Amdt. dated September 10, 2004
Response to Office Action of March 17, 2004

Amendments to the Specification:

Rewrite the paragraph at page 5, lines 15 to 20 as follows:

In an embodiment of the present invention, a digital processing system comprises a microprocessor, and the microprocessor is operable to perform a method for calling a subroutine. The method comprises the steps of: a) branching to the subroutine by executing a first instruction to provide an address of the subroutine, and b) calculating a return address by executing a second instruction to determine a relative return address. The second instruction is executed before executing the first instruction in the digital processing system.

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